UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 6,8

: 6,839,008 B1

Page 1 of 1

DATED

: January 4, 2005

INVENTOR(S) : Jered M. Sandner

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 2,

Line 5, delete "M-bit" and replace with -- 1xN-bit --;

Column 5,

Line 25, delete "WORDD[47:0] are and replace with -- WORDD[47:0] are --; Line 48, delete "" after "signal";

Column 6,

Line 51, delete "is" before "slave";

Line 59, delete "0.305" and replace with -- 305 --;

Column 7,

Lines 25, 30 and 32, delete "claim 1" and replace with -- claim 2 --;

Column 8,

Line 40, delete "up-convening" and replace with -- up-converting --.

Signed and Sealed this

Twenty-third Day of May, 2006

JON W. DUDAS
Director of the United States Patent and Trademark Office